

AMENDMENTS TO THE SPECIFICATION

IN THE ABSTRACT OF THE DISCLOSURE:

Replace the Abstract of the Disclosure currently of record with the attached new Abstract of the Disclosure appended hereto on a separate sheet.

IN THE SPECIFICATION:

Please amend the specification as follows:

Please replace the first paragraph on page 1 as follows:

This application is a divisional of application Serial No. 09/817,134, filed March 27, 2001, which is a divisional of co-pending Application No. 08/808,665, filed on February 28, 1997, the entire contents of which are hereby incorporated by reference and for which priority is claimed under 35 U.S.C. § 120; and this application claims priority of Application No. 8-194964 filed in Japan on July 24, 1996 under 35 U.S.C. § 119.

Please replace the paragraph beginning on page 7, line 15 as follows:

Clock information 116 for a program to be generated is separated from the received program multiplexed information 112 in a separating section 16 of the TS receiver 12. When receiving is started, a value of the separated clock information 116 is set in the counter 14 of the clock generating section 17, and the 27 MHz system clock is operated in the PLL 13 for counting up the counter 14. Then, the received and separated value of the clock information 116 is compared to the counter 14 by a comparator 18, a difference is given to the PLL 13, and the frequency of the system clock is corrected according to the difference. After the step and on, the same comparison and correction as those described above are executed to received and separated clock information 116, so that, by decoding coded information with the system clock 7 in the transmitting side, accurate video signals and aural signals can be regenerated. It should be noted that, in a case where fluctuation is generated in transmission of the clock information during the transmission thereof,

turbulence is generated in the regeneration of the system clock in the receiving side, and an overflow or an underflow is generated in the buffer of the decoder because of inconsistency between the frequency of the system clock in the transmitting side and that in the receiving side. As a result, turbulence is generated in a received video and a sound or the like.

Please replace the paragraph beginning on page 24, line 23 as follows:

Controls are provided so that if there is any packet containing program clock information in packets, the program clock information is preferentially multiplexed again and an error of the clock information is made to be a minimum value without any change of the clock information for transmission when each of packets ~~141a, 141b, ..., 141n~~ 131a, 131b, ..., 131n containing media information transferred from each of the control information selecting sections 31a, 31b, . . . , 31n is multiplexed again. Whether the program clock information is contained therein or not can be identified according to a PID number for the packet with clock information written in the PMT.

Please replace the paragraph beginning on page 42, line 16 as follows:

When it is to transmit the packet 133 containing the program control information transferred from the program control information editing section 32 and the packets ~~141a, 141b, ..., 141n~~ 131a, 131b, ..., 131n each containing media information transferred from each of the control information selecting section 31a, 31b, . . . , 31n as program multiplexed information 112x by multiplexing them again, the remultiplexing section 33 remultiplies the

packets described above by synchronizing to the received transmission path clock signal 144 and outputs program multiplexed information 112x.

Please replace the paragraph beginning on page 46, line 18 as follows:

Then, in the remultiplexing section 33, when the packets ~~141a, 141b, . . . , 141n~~
131a, 131b, . . . , 131n each containing media information transferred from the control information selecting sections 31a, 31b, . . . , 31n are multiplexed again, a delay in multiplication of packets is generated due to an operation for matching an output of other packet. In a case where clock information is present in a transmission-delayed packet, the remultiplexing section 33 computes the delayed time from a number of delayed bytes and an output rate from the transmission path to be outputted, adds the computed delayed time to the clock information, and transmits the packet in which an error of the clock information is corrected.

Please replace the paragraph beginning on page 50, line 19 as follows:

In FIG. 12, designated at the reference numerals 7a, 7b, . . . , 7n are program generating sections as program generating means respectively, at 8 an all program control information generating section as an all program control information generating means, at 10a, 10b, . . . , 10n the TS multiplexing transmission devices respectively, ~~at 32 the program control information editing section as a program control information editing means,~~ at 33 the remultiplexing section, at 34 the remultiplexing apparatus, and at 44a, 44b, . . . , 44n the all program control information aborting sections as all program control information aborting means respectively.